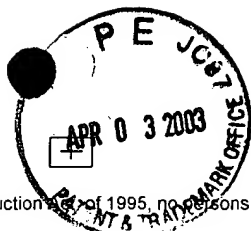


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			Application Number	09/817,696
			Filing Date	Herewith
			First Named Inventor	Howard R. Test, et al.
			Group Art Unit	2815
			Examiner Name	Richards, N.D.
Sheet 1 of 2	Attorney Docket No.	TI-30589		

U.S. PATENT DOCUMENTS						
Exam. Initials	Cite No. ¹	U.S. Patent Document		Name of Patentee or Applicant of Cited Doc.	Date of Pub. of Cited Doc. (mm-dd-yyyy)	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number	Kind Code ² (if known)			
WJL	AA	4,808,769		Nakano, et al.	02/28/1989	
WJL	AB	4,857,671		Nakano, et al.	08/15/1989	
WJL	AC	4,970,571		Yamakawa, et al.	11/13/1990	
WJL	AD	5,212,138		Krulik, et al.	05/18/1993	
WJL	AE	5,291,374		Hirata, et al.	03/01/1994	
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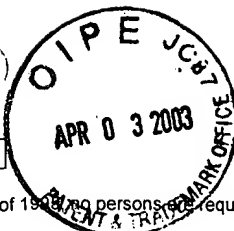
FOREIGN PATENT DOCUMENTS								
Exam. Initials*	Cite No. ¹	Foreign Patent Document			Name of Patentee or Applicant of Cited Doc.	Date of Pub. of Cited Doc. (mm-dd-yyyy)	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T ⁶
		Office ³	Number ⁴	Kind Code ² (if known)				
WJL	BA		JP 411140658 A		Hitachi Chem Co. Ltd.	25/05/1999		
	BB							
	BC							
	BD							

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				Filing Date	Herewith
				First Named Inventor	Howard R. Test, et al.
				Group Art Unit	2815
				Examiner Name	Richards, N.D.
Sheet	2	Of	2	Attorney Docket No.	TI-30589

OTHER PRIOR ART - NON PATENT LITERATURE DOCUMENTS			
Exam. Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
NS	CA	"A Low-Cost Electroless Plating Method for Producing Flip-Chip Bondable and Wire-Bondable Circuit Pads for Smart Pixel Application", Madhumita Datta, et al., IEEE, Laboratory of Physical Sciences, and Dept. of Electrical Engineering, 1998, pp. 99-100	
NS	CB	"Autocatalytic Gold Plating Process for Electronic Packaging Applications", John G. Gaudiello, IEEE, Electronic Components and Technology Conference, 1995, pp. 534-537	
NS	CC	"Break Through Developments in Electroless Nickel/Gold Plating on Copper Based Semiconductors", Andrew J. G. Strandjord, et al., 2000 International Symposium on Advanced Packaging Materials, 2001, pp. 107-111	
NS	CD	"Contact and Connection Properties of Autocatalytically 16.1 Increased Gold-Deposits", R. Freudenberger, et al., Proceedings of the Forty-Second IEEE Holm Conference on Electrical Contacts, 1996, Joint with the 18 th International Conference on Electrical Contacts, 1996, pp. 461-466	
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NS	CF	"Direct Electroless Nickel Plating on Copper Circuits Using DMAB as a Second Reducing Agent", Hideto Watanabe, et al., 1998 IEMT/IMC Proceedings, pp. 149-153	
NS	CG	"Electrolessly Deposited Diffusion Barriers for Microelectronics", E. J. O'Sullivan, et al., IBM J. Res. Develop., Vol. 42, No. 5, 09/1998, pp. 607-620	
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NS	CK	"Low Cost Flip Chip Bumping Technologies", CL Wong, et al., IEEE/CPMT Electronic Packaging Technology Conference, 1997, pp. 244-250	
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NS	CM	"Performance of Evaporated and Plated Bumps", Addi Mistry, et al., 1998 IEEE/CPMT International Electronics Manufacturing Technology Symposium, pp. 1-7	
NS	CN	"Solder Flip Chips Employing Electroless Nickel: An Evaluation of Reliability and Cost", Frank Stepniak, InterPACK '97 Final Draft, pp. 1-6	
NS	CO	"Thermosonic Gold Ball Bonding to Alternate Plating Finishes on Laminate MCM Substrates", Chris Dunn, et al., 1997 International Conference on Multichip Modules, 1997, pp. 170-176	
NS	CP	"Thermosonic Gold Wire Bonding to Palladium Finishes on Laminate Substrates", R. Wayne Johnson, et al., 1998 International Conference on Multichip Modules and High Density Packaging, pp. 291-299	
NS	CQ	"Thermosonic Gold Wirebonding to Electrolessly-Metallized Copper Bondpads Over Benzocyclobutene", Richard Ulrich, et al., 1999 International Conference on High Density Packaging and MCMs, pp. 260-265	
	CR		

Examiner Signature		Date Considered	4/17/03
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